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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEAPPLICANT **Jen et al.**FILING DATE **February 27, 2004**GROUP **2183**REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)Date Submitted to PTO: **JUNE 9, 2004**

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>mu</i>	6,081,880	Jun 27, 2000	Sollars	711	202	Mar 9, 1995
<i>mu</i>	6,230,251	May 8, 2001	Batten et al.	712	11	Mar 22, 1999
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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/ OR ABSTRACT

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>mu</i>	A. Terechko et al.; "Inter-cluster Communication Models for Clustered VLIW Processors"; <i>HPCA</i> ; 11 pages; 2002 IEEE
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<i>mu</i>	J. Zalamea et al.; "Hierarchical Clustered Register File Organization for VLIW Processors"; <i>IPDPS</i> ; 10 pages; 2003 IEEE
<i>mu</i>	P. Faraboschi et al.; "Lx: A Technology Platform For Customizable VLIW Embedded Processing"; <i>ISCA</i> ; pp. 203-213; 2000
<i>mu</i>	S. Sudharsana et al.; "Image and Video Processing Using MAJC 5200"; <i>ICIP</i> ; pp. 122-125; 2000 IEEE

EXAMINER

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4/16/2006

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